

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 3753

SERIAL NUMBE 10/709,754	FILING OR 371(c) DATE 05/26/2004 RULE	CLASS 714	G	GROUP ART UNIT 2117		ATTORNEY DOCKET NO. BUR920040001US1		
CHRISTOPH SEBASTIAN PAUL S. ZU ** CONTINUING D ** FOREIGN APPL	DHN, Richmond, VT; HER B. REYNOLDS, Unde I T. VENTRONE, South Bu CHOWSKI, JERICHO, VT; PATA ***********************************	rlington, VT; * ****						
Foreign Priority claimed 35 USC 119 (a-d) cond met Verified and Acknowledged	ter STATE COUNT VT		SHEETS TOT CLAI		MS	INDEPENDENT CLAIMS 4		
ADDRESS 24241								
TITLE A SYSTEM AND METHOD OF PROVIDING ERROR DETECTION AND CORRECTION CAPABILITY IN AN INTEGRATED CIRCUIT USING REDUNDANT LOGIC CELLS OF AN EMBEDDED FPGA								
	FEES: Authority has been given in Paper No to charge/credit DEPOSIT ACCOUNT No for following:			<u>1.1</u>	All Fees 1.16 Fees (Filing)			
RECEIVED N					☐ 1.17 Fees (Processing Ext. of time)			
1336 N					☐ 1.18 Fees (Issue) ☐ Other			
					☐ Credit			